

## OUTLINE DRAWING

### Modulation Input

Input Impedance

50 Ohms

Analog Input (SMB Male)

0 to +1.0 VDC

### RF Output

Center Frequency (Fc)

40 MHz  $\pm$  0.1%

Output Power (SMA Female)

0.5 W

Rise/Fall Time

25 nsec Typ.

RF Contrast Ratio

35 dB min

Harmonic Distortion

-20 dBc

Output Impedance

50 Ohms

Output VSWR

1.5 : 1 Max

Bandwidth

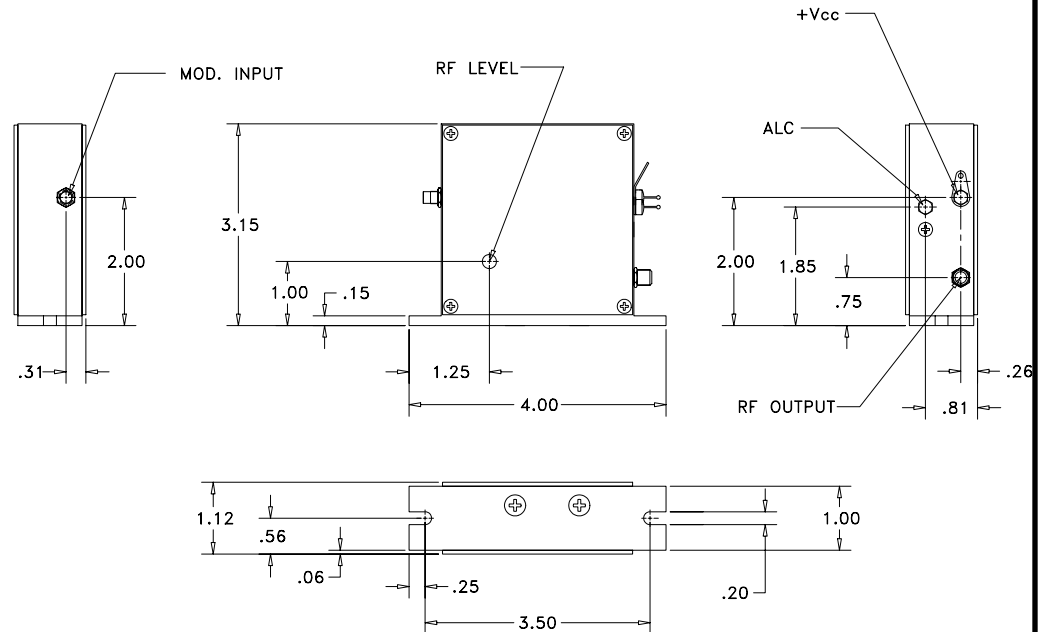
20 MHz

Power Supply Voltage (Filtered Feedthru)

+24 V @ 550 mA

ALC Voltage Level (Filtered Feedthru)

+3.5 to +21 V nominal



### Notes:

1. The slope of the RF output power vs. the input signal voltage curve shall be non-zero and positive at all points between 0 and 1.0 Volts input, inclusive.
2. Output power factory set to 0.5 W at 1 Volt input. Power stability less than 5% over the heat sink's ambient temperature range of 0-40° C, after 5 minute warm-up.
3. When calculating the contrast ratio, it is understood that only the power of the 40 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.
4. A +21 Volt nominal input on the ALC corresponds to full RF output power. Zero RF power occurs at an ALC voltage slightly above +3.5 Volts. Full RF power occurs if ALC input is left unconnected.

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TOLERANCES: .XX $\pm$ .01 .XXX $\pm$ .005	DR	A. Campi 10/23/2014			
MATERIAL: 	CHK		DESCRIPTION: <b>AODR 1040AF-AIF0-0.5</b>		
FINIS: 	APP				
	APP		PART NUMBER: <b>97-03307-74</b>	REV: <b>1</b>	1 of 1