

#### **Digital Driver**



**Features** 

High output voltage 12 V<sub>DD</sub>

High gain 35 dB

High SNR

Single voltage power supply

#### **Applications**

LiNbO<sub>3</sub> & InP modulators

10 Gbps DPSK

2×10 Gbps (D)QPSK

Research & Development

#### **Options**

Heat-sink

Alternative RF connectors

The DR-DG-10-HO is a driver module optimized for digital applications requiring an upper operation voltage at 10 Gbps - 12.5 Gbps. It exhibits 12.5  $V_{\rm nn}$  output volatge and 35 dB gain up to 7 GHz.

The DR-DG-10-HO module is especially useful for driving  $LiNb0_3$  modulators with 10 Gbps DPSK and 2 x 10 Gbps (D)QPSK modulation formats.

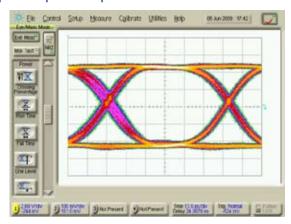
It is also a key device for multi-level modulation formats and for driving phase modulators. It is operated from a single power supply voltage for safety and ease of use and offers output voltage control. The DR-DG-10-HO comes with SMA type RF connectors (female in, male out) and with an optional heat sink. It is a non-inverting and single ended amplifier.

#### Performance Highlights

| Parameter           | Min  | Тур  | Max | Unit |
|---------------------|------|------|-----|------|
| Cut-off frequencies | 80 k | -    | 7 G | Hz   |
| Output Voltage      | -    | 12.5 | -   | V    |
| Gain                | -    | 35   | -   | dB   |
| Saturated Power     | 26   | -    | -   | dBm  |
| Added Jitter        | -    | 1.75 | -   | ps   |
| Rise / Fall Times   | -    | 24.5 | -   | ps   |

Measurements for  $V_{bias}$  = 12 V,  $V_{amp}$  = 1.2 V,  $I_{bias}$  = 420 mA

#### 12.5 Gbps Output Response





#### **Digital Driver**

#### **DC Electrical Characteristics**

| Parameter              | Symbol            | Min | Тур   | Max | Unit |
|------------------------|-------------------|-----|-------|-----|------|
| Supply voltage (fixed) | V <sub>bias</sub> | -   | 12    | -   | V    |
| Current consumption    | bias              | -   | 0.420 | -   | А    |
| Gain control voltage   | V <sub>amp</sub>  | 0   | 1.4   | -   | V    |

#### **Electrical Characteristics**

Conditions:  $V_{in} = 0.5 V_{pp}$ ,  $T_{amb} = 25 ^{\circ}C$ , 50  $\Omega$  system

| Parameter             | Symbol                          | Condition  | Min | Тур         | Max | Unit            |
|-----------------------|---------------------------------|--|-----|-------------|-----|-----------------|
| Lower frequency       | f <sub>3dB</sub> , lower        | -3 dB point  | -   | -           | 80  | kHz             |
| Upper frequency       | f <sub>3dB</sub> , upper        | -3 dB point  | -   | 7           | -   | GHz             |
| Gain                  | S <sub>21</sub>                 | Small signal   | -   | 35          | -   | dB              |
| Gain ripple           | -                               | < 18 GHz   | -   | ±1.5        | -   | dB              |
| Input return loss     | S <sub>11</sub>                 | 10 MHz < f < 10 GHz  | -   | -10         | -   | dB              |
| Output return loss    | S <sub>22</sub>                 | 10 MHz < f < 10 GHz  | -   | -10         | -   | dB              |
| Output voltage        | V <sub>out</sub>                | V <sub>in</sub> = 0.5 V <sub>pp</sub> @ 10.7 Gbps              | -   | 12          | -   | V <sub>pp</sub> |
| Rise time / Fall time | t <sub>r</sub> / t <sub>f</sub> | 20 % - 80 %  | -   | 24.5 / 24.5 | -   | ps              |
| Added jitter          | J <sub>RMS</sub>                | J <sub>RMS</sub> = $\sqrt{J_{RMS-total}^2 - J_{RMS-source}^2}$ | -   | 1.9         | -   | ps              |
| Power dissipation     | Р                               | V <sub>out</sub> = 12 V <sub>pp</sub>                          | -   | 5           | -   | W               |

#### **Absolute Maximum Ratings**

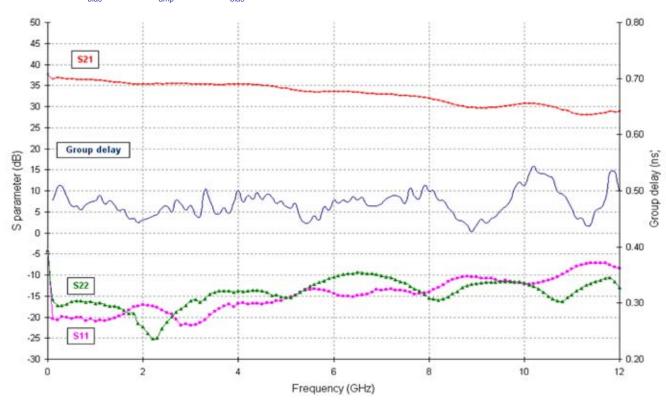
Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

| Parameter                | Symbol            | Min  | Max  | Unit            |
|--------------------------|-------------------|------|------|-----------------|
| RF input voltage         | V <sub>in</sub>   | -    | 1.5  | V <sub>pp</sub> |
| Power supply voltage     | V <sub>bias</sub> | 11.5 | 13   | V               |
| DC current               | bias              | -    | 0.45 | А               |
| Gain control voltage     | V <sub>amp</sub>  | 0    | 2    | V               |
| Power dissipation        | P <sub>diss</sub> | -    | 9.8  | W               |
| Temperature of operation | T <sub>op</sub>   | -5   | +50  | °C              |



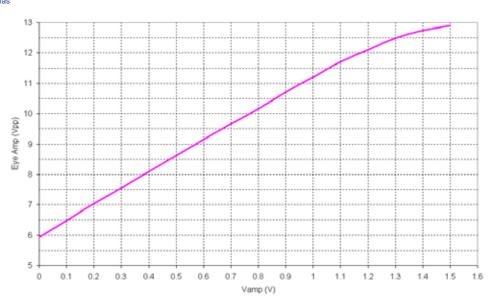
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S parameters curves Conditions:  $V_{bias}$  = 12 V,  $V_{amp}$  = 0.2 V,  $I_{bias}$  = 420 mA



### Typical output voltage amplitude vs gain control $V_{\text{amp}}$ tuning

Conditions: V<sub>bias</sub> = 12 V



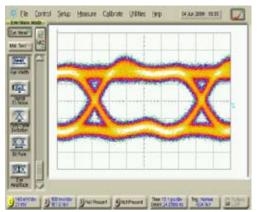


#### **Digital Driver**

#### **Eye Diagrams**

#### 10.709 Gbps data rate

Conditions: Ratio  $\frac{1}{2}$ , Pattern 2<sup>31</sup>-1  $V_{bias}$  = 12 V,  $V_{amp}$  = 1.2 V,  $I_{bias}$  = 420 mA

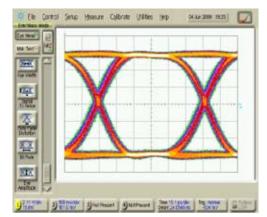


#### Input signal

Genrated by Anritsu MP1758A

Eye amplitude = 0.44 V, Rise time = 17.4 ps

Jitter RMS = 2.14 ps, SNR = 8.1

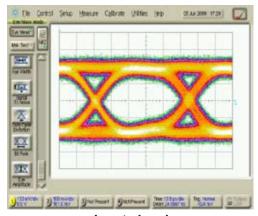


#### **Output response**

Measured using Agilent 86100B with two 50 GHz
8348A channels module, and without precision time-base module
Eye amplitude = 12.1 V, Rise time = 24.8 ps
Jitter RMS = 1.67 ps, SNR = 43.5

#### 12.5 Gbps data rate

Conditions: Ratio  $\frac{1}{2}$ , Pattern 2<sup>31</sup>-1 V<sub>bias</sub> = 12 V, V<sub>amp</sub> = 1.2 V, I<sub>bias</sub> = 420 mA

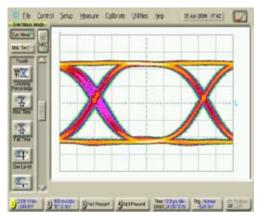


#### Input signal

Generated with a NEL MOF15A 2:1 selector

Eye amplitude = 0.49 V, Rise time = 23 ps

Jitter RMS = 2.24 ps, SNR = 8.8



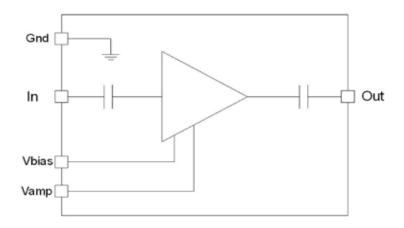
#### **Output response**

Measured using Agilent 86100B with two 50 GHz
8348A channels module, and without precision time-base module
Eye amplitude = 12.1 V, Rise time = 25.3 ps
Jitter RMS = 2.5 ps, SNR = 32.77



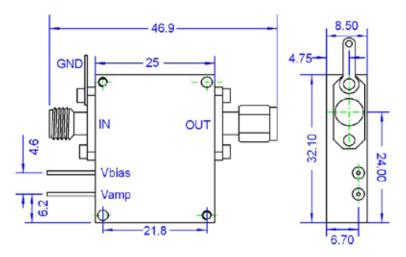
**Digital Driver** 

#### **Electrical Schematic Diagram**



#### Mechanical diagram and pinout

All measurements in mm





The heatsinking of the module is necessary. It's user responsability to use an adequate heatsink. Refer to page 6 for Photline Technologies recommended heatsink.

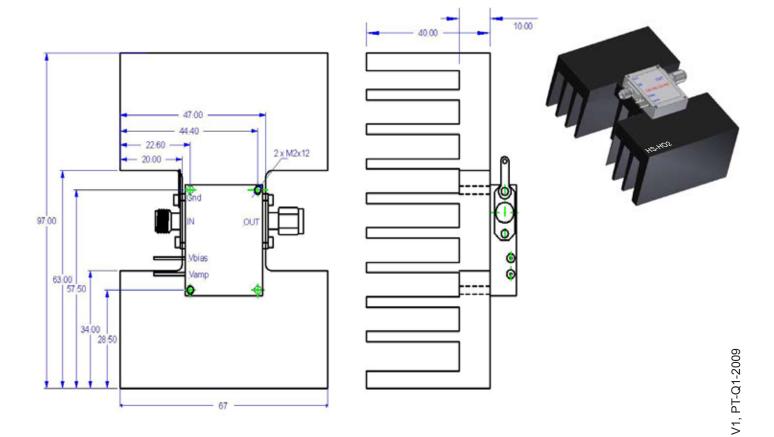
| PIN               | Function                            | Operational Notes                      |
|-------------------|-------------------------------------|--|
| IN                | RF In                               | SMA-connector female                   |
| OUT               | RF Out                              | SMA-connector male                     |
| V <sub>bias</sub> | Power supply voltage                | Set at typical operating specification |
| V <sub>amp</sub>  | Output voltage amplitude adjustment | Adjust for gain control tuning         |



**Digital Driver** 

### Mechanical diagram and pinout with HS-HO2 heatsink

All measurements in mm



#### **ABOUT US**

Photline Technologies is a provider of Fiber Optics Modulation Solutions based on the company LiNb03 modulators and high-speed electronics modules. Photline Technologies offers high speed and high data rate modulation solutions for the telecommunication industry and the defense, aerospace, instruments and sensors markets. The products offered by the company include: comprehensive range of intensity and phase modulators (800 nm, 1060 nm, 1300 nm, 1550 nm), RF drivers and modules, transmitters and modulation units.

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