

**DFF** 



**Features** 

- Data transmission up to 30 Gbps
- 3.3 V to 8 V single voltage power supply
- Low jitter
- Low rise / fall time
- Single ended and differential output

### **Applications**

- Data retiming
- 28 Gbps DPSK
- 2×28 Gbps (D)QPSK
- Research & Development

### **Options**

Alternative RF output connectors

The DFF-DG-30 is a D-type Flip Flop (DFF) module which is primarily intended for retiming of high data rate signals. The DFF-DG-30 supports data transmission rates up to 30 Gbps and clock frequencies as high as 30 GHz.

The DFF-DG-30 retimes and reshapes single ended input data streams into differential output data streams.

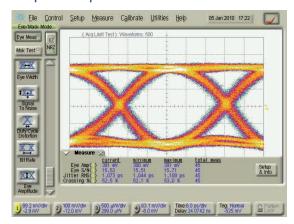
It is also useful when associated with other logical circuits for application such as: NRZ/RZ conversion, DPSK and DQPSK differential encoding, phase detection in PLL loops or memories.

#### Performance Highlights

Parameter	Min	Тур	Max	Unit
Data rate	2	-	30	Gbps
Data output voltage (single-ended)	-	390	-	mV
SNR	-	15	-	-
СРМ	-	200	-	Degree
Rise / Fall Times	-	13 / 13	-	ps

Measurements for  $V_{bias} = 5 \text{ V}$ ,  $I_{bias} = 136 \text{ mA}$ 

#### 30 Gbps Output Response



## DFF-DG-30 30 Gbps D-type Flip-Flop Module



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## **DC Electrical Characteristics**

Parame	eter	Symbol	Condition	Min	Тур	Max	Unit
Supply	voltage	V <sub>bias</sub>		3.3	5	8	V
Currer	nt consumption	l <sub>bias</sub>	V <sub>bias</sub> = 5 V	-	0.135	-	А

# Electrical Characteristics Conditions: $V_{bias} = 5 \text{ V}$ , $T_{amb} = 25 \text{ °C}$ , 50 $\Omega$ system

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Data rate	D <sub>in</sub>	NRZ format @f <sub>Clk</sub> 2 - 30		30	Gbps	
Clock frequency	f <sub>CIk</sub>	-	2	-	30	GHz
Data input voltage	V <sub>Din</sub>	-	100	300	800	$mV_{pp}$
Clock input voltage	V <sub>Clk</sub>	-	100	300	800	dB
Data autout valtage	V <sub>Dout</sub>	Single-ended	370	390	420	
Data output voltage	V <sub>Dout+</sub> - V <sub>Dout-</sub>	Differential	740	780	840	mV <sub>pp</sub>
Data input return loss	S11 <sub>Din</sub>	f < 10 GHz	-	-	-10	dB
Clock input return loss	S11 <sub>Clk</sub>	f < 10 GHz	-	-	-10	dB
Data output return loss	S22 <sub>Dout</sub>	f < 10 GHz	-	-	-10	dB
Signal noise ratio	SNR	-	-	15	-	-
Clock phase margin	СРМ	@28 Gbps	-	200	-	Degree
Rise time / Fall time	t <sub>r</sub> / t <sub>r</sub>	20 % - 80 %, 28 Gbps	-	13 / 13	-	ps
Power dissipation	P <sub>diss</sub>	V <sub>bias</sub> = 5 V 0.675 -		-	W	

# **Absolute Maximum Ratings**

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Symbol	Min	Max	Unit
Data input voltage	D <sub>in</sub>	0	800	mV <sub>pp</sub>
Clock input voltage	V <sub>Clk</sub>	0	800	mV <sub>pp</sub>
Supply voltage	V <sub>bias</sub>	0	8	V
Power dissipation	P <sub>diss</sub>	-	0.7	W
Temperature of operation	T <sub>op</sub>	0	+40	°C
Storage temperature	T <sub>st</sub>	-20	+70	°C

## DFF-DG-30 30 Gbps D-type Flip-Flop Module

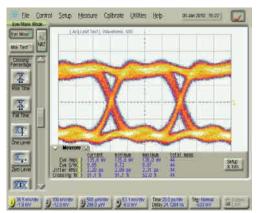


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# **Example of Reshaped Eye Diagrams**

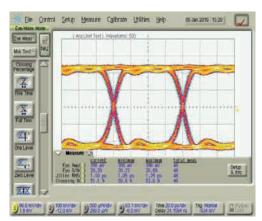
## 12.5 Gbps data rate

Conditions: Ratio  $\frac{1}{2}$ , Pattern 2<sup>31</sup>-1 V<sub>bias</sub> = 5 V, I<sub>bias</sub> = 136 mA



#### Input signal

Eye amplitude = 0.135 V, Rise time = 15 ps Jitter RMS = 2.2 ps, SNR = 8.85

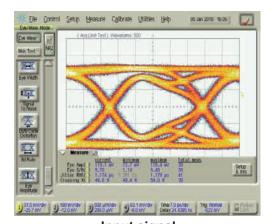


#### Output reshaped signal

Measured using Agilent 86100B with two 50 GHz
8348A channels module, and without precision time-base module
Eye amplitude = 0.395 V, Rise time = 12.9 ps
Jitter RMS = 1.1 ps, SNR = 20.2

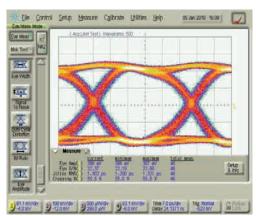
#### 25 Gbps data rate

Conditions: Ratio  $\frac{1}{2}$ , Pattern 2<sup>31</sup>-1 V<sub>bias</sub> = 5 V, I<sub>bias</sub> = 136 mA



## Input signal

Eye amplitude = 0.118 V, Rise time = 13.1 ps Jitter RMS = 1.17 ps, SNR = 5.7



#### Output reshaped signal

Measured using Agilent 86100B with two 50 GHz 8348A channels module, and without precision time-base module Eye amplitude = 0.386 V, Rise time = 12.6 ps Jitter RMS = 1.3 ps, SNR = 22.37

## DFF-DG-30 30 Gbps D-type Flip-Flop Module

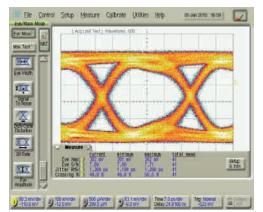


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# **Example of Reshaped Eye Diagrams**

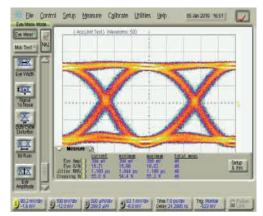
#### 28 Gbps data rate

Conditions: Ratio  $\frac{1}{2}$ , Pattern 2<sup>31</sup>-1 V<sub>bias</sub> = 5 V, I<sub>bias</sub> = 136 mA



#### Input signal

Eye amplitude = 0.382 V, Rise time = 11.36 ps Jitter RMS = 1.27 ps, SNR = 7.69

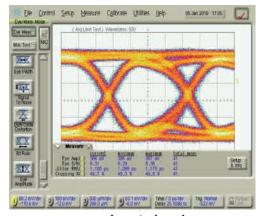


#### **Output reshaped signal**

Measured using Agilent 86100B with two 50 GHz
8348A channels module, and without precision time-base module
Eye amplitude = 0.394 V, Rise time = 13.07 ps
Jitter RMS = 1.16 ps, SNR = 15.7

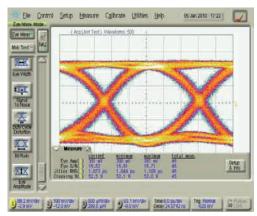
#### 30 Gbps data rate

Conditions: Ratio  $\frac{1}{2}$ , Pattern 2<sup>31</sup>-1 V<sub>bias</sub> = 5 V, I<sub>bias</sub> = 136 mA



#### Input signal

Eye amplitude = 0.386 mV, Rise time = 11.36 ps Jitter RMS = 1.1 ps, SNR = 8.31



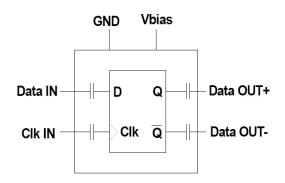
#### Output reshaped signal

Measured using Agilent 86100B with two 50 GHz
8348A channels module, and without precision time-base module
Eye amplitude = 0.391 V, Rise time = 13.73 ps
Jitter RMS = 1.073 ps, SNR = 15.52



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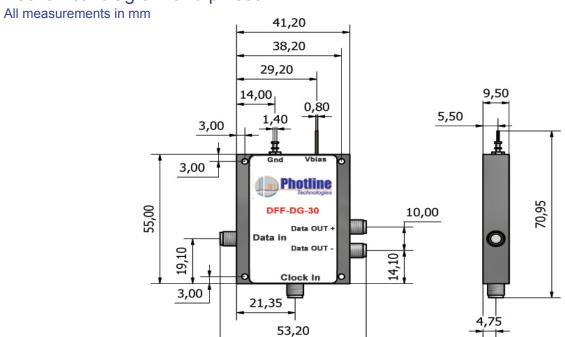
# **Electrical Schematic Diagram**



Clock	DATA IN	DATA OUT	DATA OUT previous
Rising edge 1	0	0	X
Rising edge 1	1	1	Х
No clock or non-rising edge	×	Previous state	х

X: "don't care" condition (signal is irrelevant)

# Mechanical diagram and pinout



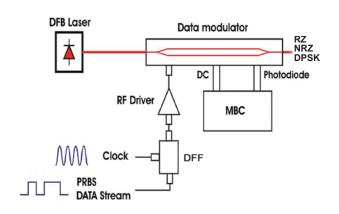
PIN	Function	Operational Notes
Data In	Data RF input	K-connector female
Clock In	Clock RF input	K-connector female
V <sub>bias</sub>	Power supply voltage	Pin feed through diameter 0.8 mm
Gnd	Ground	Pin feed through diameter 1.4 mm
Data Out -	RF output	K-connector female (male in option)
Data Out +	RF output	K-connector female (male in option)

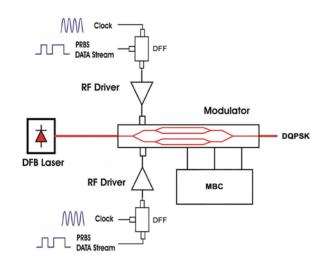
# DFF-DG-3022 Gbps High Output Voltage Driver Module



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## Related equipments





## RZ / NRZ / DPSK datastream reshaping

MX-LN series modulators are intended for 10 / 28 / 40 Gbps RZ / NRZ / DPSK modulation formats.

DR-DG amplifiers series are intended to drive MX-LN series modulators for 10 / 28 / 40 Gbps RZ / NRZ / DPSK modulation formats.

MBC-DG-BT is an automatic bias controller designed to lock the operating point of the MX-LN modulators.



MXIQ-LN-40 is an ultra low loss IQ modulator for 2  $\times$  22 Gbps DQPSK modulation.

DQPSK datastream reshaping





DR-DG-20-HO amplifiers are intended to drive MXIQ-LN-40 modulator with 2 x V $_\pi$  signal for 2 x 22 Gbps DQPSK modulation.

MBC-IQ-BT is an automatic bias controller designed to individually lock the operating point of the MXIQ-LN-40 sub-MZs.

#### **ABOUT US**

Photline Technologies is a provider of Fiber Optics Modulation Solutions based on the company LiNb03 modulators and high-speed electronics modules. Photline Technologies offers high speed and high data rate modulation solutions for the telecommunication industry and the defense, aerospace, instruments and sensors markets. The products offered by the company include: comprehensive range of intensity and phase modulators (800 nm, 1060 nm, 1300 nm, 1550 nm), RF drivers and modules, transmitters and modulation units.

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