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Optical Network Transceiver Innovator

# GTTC-XXXX3G-L4CD 3Gbps Video SFP Optical Transmitter, 40km Reach

#### **Features**

- ♦ HD-SDI SFP Transmitter available
- ♦ SD-SDI SFP Transmitter available
- ♦ 3G-SDI SFP Transmitter available
- ♦ SMPTE 297-2006 Compatible.
- ♦ Metal enclosure for Lower EMI
- ♦ 18 CWDM DFB laser
- Supports video pathological patterns for SD-SDI, HD-SDI and 3G-SDI
- Digital Diagnostic functions available through the I2C interface
- Compatible with RoHS
- ♦ +3.3V single power supply
- Operating case temperature:

Standard: 0 to +70°C

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#### **Applications**

- SMPTE 297-2006 Compatible Electrical-to-Optical Interfaces.
- ♦ HDTV/SDTV Service Interfaces.

#### **Description**

The video series transceivers are high performance, cost effective modules for duplex video transmission application over single mode fiber.

The Transmitter is designed to transmit data rates from 50Mbps to 2.97Gbps and is specifically designed for robust performance in the presence of SDI pathological patterns for SMPTE 259M, SMPTE 344M, SMPTE 292M and SMPTE 424M serial rates. The module is fully compliant with SMPTE 297M-2006.

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The transmitter is a dual channel optical transmitter module ,one channel consists of two sections: a DFB laser transmitter and MCU control unit. All modules satisfy class I laser safety requirements.

**Absolute Maximum Ratings** 

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

**Recommended Operating Conditions** 

Parameter	Symbol	Min	Typical	Max	Unit	
Operating Case Temperature	Standard	Тс	0		+70	°C
Operating case remperature						°C
Power Supply Voltage	Vcc	3.13	3.3	3.47	V	
Power Supply Current		Icc			500	mA
Data Rate			3		Gbps	

**Optical and Electrical Characteristics** 

Paran	neter	Syn	nbol	Min	Typical	Max	Unit	Notes	
	Transmitter								
Cer	ntre Wavelengt	h	λc	λc-6.5	λς	λ c+6.5	nm		
Spec	tral Width (-20d	dB)	σ			1	nm		
Side Mod	de Suppressior	Ratio	SMSR	30			dB		
Avera	Average Output Power		Pout	-2	0	+3	dBm	1	
E	Extinction Ratio		ER	5	8		dB		
D: /F		SD-SDI	. 46			1500			
	all Time ~80%)	HD-SDI	tr/tf			270	ps	2	
(2070	(2070 0070)					135			
Total	PRBS and	SD-SDI			70	200	no		
Output	colour	HD-SDI			50	135	ps		

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Jitter	bar	3G-SDI			70	100		
		SD-SDI			200	300		
	pathological	HD-SDI			115			
		3G-SDI			120			
Data In	Data Input Swing Differential		V <sub>IN</sub>	400		1800	mV	3
Input D	Input Differential Impedance		Z <sub>IN</sub>	90	100	110	Ω	
TX Disable	Disab	le		2.0		Vcc	V	
17 Bloable	Enab	le		0		0.8	V	
Fa TX Fault		t		2.0		Vcc	V	
	Norm	al		0		0.8	V	

#### Notes:

- 1. The optical power is launched into SMF.
- 2. Rise and fall times, 20% to 80%, are measured following a fourth-order Bessel-Thompson filter with a bandwidth of 0.75 x clock frequency corresponding to the serial data rate
- 3. PECL input, internally AC-coupled and terminated.
- 4. Internally AC-coupled.

**Timing and Electrical** 

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
Serial ID Clock Rate	f_serial_clock			280	KHz
MOD_DEF (0:2)-High	V <sub>H</sub>	2		Vcc	V
MOD_DEF (0:2)-Low	$V_L$			0.8	V



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**Diagnostics Specification** 

Diagnostics opecification								
Parameter	Range	Unit	Accuracy	Calibration				
Temperature	0 to +70	°C	±3°C	Internal / External				
Voltage	3.0 to 3.6	V	±3%	Internal / External				
Bias Current	0 to 100	mA	±10%	Internal / External				
TX Power	-5 to 0	dBm	±3dB	Internal / External				

## **I2C Bus Interface**

The I2C bus interface uses the 2-wire serial CMOS E2PROM protocol. The serial interface meets the following specifications:

- 1. Support a maximum clock rate of 280Khz.
- 2. Input/Output levels comply with LVCMOS/LVTTL or compatible logics.

Low: 0 – 0.8 V High: 2.0 – 3.3 V Undefined: 0.8 – 2.0 V

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#### **Pin Definitions**

Pin Diagram

Top of Board

20	TX1_DIS
19	TD1-
18	TD1+
17	VEE_TX1
16	VCC_TX1
15	VCC_TX2
14	VEE_TX2
13	NC
12	TX2_FAULT
11	VEE_TX2

Bottom of Board (as viewed through top of board)

1	VEE_TX1
2	TX1_FAULT
3	NC
4	VEE_TX1
5	I <sup>2</sup> C CLK
6	I <sup>2</sup> C DATA
7	VEE_TX2
8	TD2+
9	TD2-
10	TX2_DIS

**Pin Descriptions** 

Pin	Signal Name	Description	Plug Seq.	Notes
1	VEE_TX1	Transmitter 1 Ground	1	
2	TX1_ FAULT	Transmitter 1 Fault Indication	3	Note 1

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3	NC	Not Connected	3	
4	VEE_TX1	Transmitter 1 Ground	3	
5	I2C CLK	SCL Serial Clock Signal	3	Note 3
6	I2C DATA	SDA Serial Data Signal	3	Note 3
7	VEE_TX2	Transmitter 2 Ground	3	
8	TD2+	Transmit 2 Data In	3	Note 4
9	TD2-	Inv. Transmit 2 Data In	1	Note 4
10	TX2_DIS	Transmitter 2 Disable	1	Note 2
11	VEE_TX2	Transmitter 2 Ground	1	
12	TX2_FAULT	Transmitter 2 Fault Indication	3	Note 1
13	NC	Not Connected	3	
14	VEE_TX2	Transmitter 2 Ground	1	
15	VCC_TX2	Transmitter Power 2 Supply	2	
16	VCC_TX1	Transmitter Power 1 Supply	2	
17	VEE_TX1	Transmitter 1 Ground	1	
18	TD1+	Transmit 1 Data In	3	Note 4
19	TD1-	Inv. Transmit 1 Data In	3	Note 4
20	TX1_DIS	Transmitter 1 Disable	1	Note 2

#### Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a  $4.7k\sim10k\Omega$  resistor. Its states are:

Low (0 to 0.8V): Transmitter on (>0.8V, < 2.0V): Undefined

High (2.0 to 3.465V): Transmitter Disabled Open: Transmitter Disabled

- 3) They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be VCC\_TX1or VCC\_TX2. I2C CLK is the clock line of two wire serial interface for serial ID I2C DATA is the data line of two wire serial interface for serial ID
- 4) TD1/2-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

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CWDM Wavelength (0~70°C)

Band	Suffix	Wavelength (nm)
	A	1270
	В	1290
O-band Original	С	1310
	D	1330
	Е	1350
	F	1370
	G	1390
E-band Extended	Н	1410
	I	1430
	Ј	1450
	K	1470
S-band Short Wavelength	L	1490
5 band bhort waverength	M	1510
	N	1530
C-band Conventional	0	1550
	P	1570
L-band Long Wavelength	Q	1590
	R	1610

### **Serial ID Field Memory Map**

The module serial Id and calibration information is stored in the E2PROM of the SFP supervising device using the address map.

Byte Addr	Bit Size	Name	Description	Value (hex)
0	1	Identifier	Type of transceiver	82
1	1	Ext. Identifier	Extended identifier of type of transceiver	04
2	1	Connector	Code for connector type	07
3	1	Standards Compliance	For SMPTE259M/344M/292M/424M and SMPTE 297M	41

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4				
5				
6				
7	7	Transceiver	Code for electronic or optical compatibility,	
8			Not applicable.	
9				
10				
11	1	Encoding	Code for serial encoding algorithm	30
12	1	BR, Nominal	Nominal signalling rate, units of 100MBd.	1E
13	1	Rate Identifier	Type of rate select functionality, Not applicable	
14	1	Length(SMF, km)	Link length supported for single mode fiber, units of $km$	14
15	1	Length (SMF)	Link length supported for single mode fiber, units of 100 $\ensuremath{\mathrm{m}}$	00
16	1	Length (50um)	Link length supported for 50 um 0M2 fiber, units of 10 $\ensuremath{\mathrm{m}}$	00
17	1	Length (62.5um)	Link length supported for $62.5$ um $0\text{M1}$ fiber, units of $10~\text{m}$	00
18	1	Length (cable)	Link length supported for copper or direct attach cable, units of $\ensuremath{\mathtt{m}}$	00
19	1	Length (OM3)  Link length supported for 50 um OM3 fiber, units of 10 m		00
20		.6 Vendor name		
21			SFP vendor name (ASCII)	X
22	23 24 25 26			X
				X
				X
				X
				X
27				X
28				X
29				X

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_				
30				X
31				X
32				X
33				X
34				X
35				X
36	1	Reserved	Reserved	00
37				00
38	3	Vendor OUI	SFP vendor IEEE company ID	00
39				00
40				X
41				X
42			Don't grow and an area (ASCII)	X
43				X
44				X
45				X
46				X
47	16	Vandar DN		X
48	16	Vendor PN	Part number provided by SFP vendor (ASCII)	X
49				X
50				X
51				X
52				X
53				X
54				X
55				
56		4 Vendor rev	Revision level for part number provided by vendor (ASCII)	
57	4			X
58				
59				
60	2	Wavelength	Laser wavelength (Passive/Active Cable Specification Compliance)	
61			opecification compitance)	

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62	1	Unallocated		
63	1	CC_BASE	Check code for Base ID Fields	
64 65	2	Options	Indicates which optional transceiver signals are implemented	
66	1	BR, max	Upper bit rate margin, units of %	05
67	1	BR, min	Lower bit rate margin, units of %	5F
68				X
69				X
70				X
71				X
72 <b>7</b> 2				X
73				X
74 75		Vendor SN		X X
76	16		Serial number provided by vendor (ASCII)	X
77				X
78				X
79				X
80				X
81				X
82				X
83 84				X
85			Vendor's manufacturing date code	
86		Date code		
87	8			
88	O			
89				
90				
91		D:		
92	1	Diagnostic Monitoring Type	Indicates which type of diagnostic monitoring is implemented(if any) in the transceiver	28
93	1	Enhanced Options  Indicates which optional enhanced features are implemented (if any) in the transceiver		90
94	1	SFF-8472Compliance	Indicates which revision of SFF-8472 the transceiver complies with.	X

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95	1	CC_EXT	Check code for the Extended ID Fields	
96				0
97				0
98				0
99				0
100				0
101				0
102				0
103				0
104				0
105				0
106				0
107				0
108				0
109				0
110				0
111	32	Vendor Specific	Vendor Specific EEPROM	0
112		poor and		0
113				0
114				0
115				0
116				0
117				0
118				0
119				0
120 121				0
121				0
122				0
123				0
124				0
126				0
127				0

**Digital Diagnostic Monitoring Interface (2-Wire Address A2H)** 

Byte Addr Bit Size Name Description and Value of the Field

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4		
4		
2	TX_I (Slope)	Set to 1 for "internally calibrated" devices. Value is 01 00.
2	TX_I (Offset)	Set to zero for "internally calibrated" devices. Value is 00 00.
2	TX_PWR (Slope)	Set to 1 for "internally calibrated" devices. Value is 01 00.
2	TX_PWR (Offset)	Set to zero for "internally calibrated" devices. Value is 00 00.
2	T (Slope)	Set to 1 for "internally calibrated" devices. Value is 01 00.
2	T (Offset)	Set to zero for "internally calibrated" devices. Value is 00 00.
2	V (Slope)	Set to 1 for "internally calibrated" devices. Value is 01 00.
2	V (Offset)	Set to zero for "internally calibrated" devices. Value is 00 00.
3	Reserved	Reserved
1	Checksum	Checksum of bytes 0 - 94.
2	Temperature (MSB, LSB)	Internally measured module temperature
2	Supply Voltage (MSB, LSB)	nternally measured supply voltage in module
2	Bias1()(MSB, LSB)	Internally measured module bias1
2	Tx1 Power (MSB, LSB)	Internally measured Tx1 Power Current
2	Tx2 Power (MSB, LSB)	Internally Measured Tx2 Power Current
2	Bias2()(MSB, LSB)	Internally measured module bias2
2	Reserved	Reserved
Bit7	Tx Disable State	Digital state of the TX Disable Input Pin.
Bit6	Soft Tx Disable	Bit 6
Bit5-Bit3	Reserved	
	4 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 8it7 Bit6	4         2       TX_I (Slope)         2       TX_PWR (Slope)         2       TX_PWR (Offset)         2       T (Slope)         2       T (Offset)         2       V (Slope)         2       V (Offset)         3       Reserved         1       Checksum         2       Temperature (MSB, LSB)         2       Supply Voltage (MSB, LSB)         2       Bias1() (MSB, LSB)         2       Tx1 Power (MSB, LSB)         2       Tx2 Power (MSB, LSB)         2       Bias2() (MSB, LSB)         2       Reserved         Bit7       Tx Disable State         Bit6       Soft Tx Disable

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110	Bit2	Tx Fault	Bit 2
110	Bit1		Bit1
110	Bit0	Data_Ready	Bit 0
111	1	Reserved	Reserved
112	1		
113	1		
114-115	Reserved		Reserved
116	1		
117	1		
118-119	2	Reserved	Reserved
120-127	8	Vendor specific	
128-247	120	User EEPROM	User writable EEPROM
248-255	8	Vendor Specific	Vendor specific control functions

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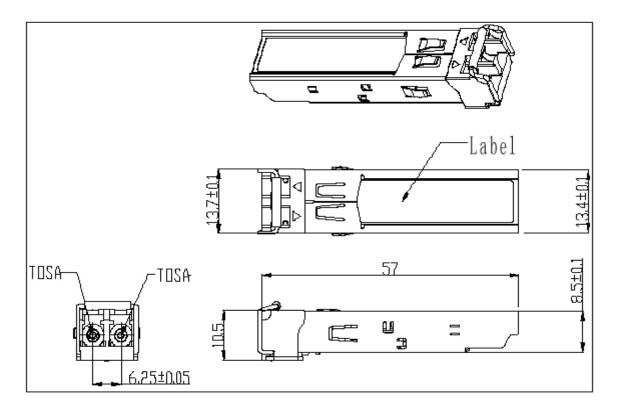
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#### **Mechanical Dimensions**



# **Ordering information**

Part Number		Product Description
GTTC-XXXX3G-L4CD	CWDM, 3Gbps, 40km,	0°C ~ +70°C, With Digital Diagnostic Monitoring

#### **Important Notice**

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